

PRELIMINARY AMENDMENT

Serial Number: 08/902,133

Filing Date: July 29, 1997

Title: DYNAMIC ELECTRICALLY ALTERABLE PROGRAMMABLE READ ONLY MEMORY DEVICE

Page 14

Dkt: 303.356US1

56.  
72.(New) A memory device comprising:

an array of memory cells, each memory cell comprising:

a source region in a substrate;

a drain region in the substrate;

a channel region in the substrate between the source region and the drain region;

and

means for storing charge;

a row decoder;

a column decoder;

a command and control circuit; and

a voltage control circuit.

REMARKS

The applicant has amended claims 18, 19, 29, 32, and 39 and added new claims 41-72 in this amendment. The applicant respectfully submits that all of the pending claims are in condition for allowance and such action is earnestly solicited. The Examiner is invited to telephone the below-signed attorney at 612-373-6973 to discuss any questions which may remain with respect to the present application.

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6973

Date 15 MAR 2000

By

Robert E. Mates

Reg. No. 35,271

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner of Patents, Washington, D.C. 20231 on March 15, 2000.

Name

Signature